equipment to regulate the audio level of an output signal from said audio equipment to a predetermined range, and an output terminal;

a signal processing circuit having an input terminal connected to the output terminal of said automatic audio level regulating circuit, and an output terminal;

[an (external and internal)] a dual adjustable oscillatory frequency regulating circuit comprising an oscillator transistor, and dielectric resonator, a first variable resistor [capacitor], a second variable [capacitor diode] resistor and a variable capacitor diode, an input terminal connected to the output terminal of said signal processing circuit, and an output terminal; a [first intermediate] carrier frequency output being [at least about 10MHz] adjusted and set by said first and second variable [capacitor] resistors.

an inductance antenna connected to the output terminal of said [(external and internal)] dual adjustable oscillatory frequency regulating circuit, said inductance antenna being a matching device; and

a power control circuit controlled by the output signal of said audio equipment to provide the necessary working voltage to said transmitter [unit], said power control circuit comprising a signal amplifier, a comparator and a transistor switch, so that when said signal amplifier receives an input signal from said audio equipment it drives said comparator and transistor switch permitting the connection of an external power supply or battery supply to said [transmitting unit] transmitter.

20. A receiver for a wireless transmitter-receiver system wherein the system transmitter includes an inductance antenna and is adapted to be coupled to audio equipment to transmit an audio signal therefrom through an inductance antenna comprising:

a receiving antenna adapted to receive an audio signal transmitted from [an] the inductance antenna of said transmitter [unit].

[an (external and internal)] a dual adjustable oscillatory frequency regulating circuit comprising an oscillatory transistor, a dielectric resonator, and a first variable [capacitor] resistor, a second variable resistor, and a variable capacitor diode, an input terminal connected to the output terminal of said receiving antenna, and an output terminal;

a signal processing circuit connected to said [(]external and internal[)] dual adjustable oscillatory frequency regulating circuit to process received signals and to provide a processed signal to said [earphone] receiver;

an automatic 24-time frequency divider circuit comprising a resistor and an oscillator , connected to [an] a first IC of said receiver signal processing circuit to divide the frequency of said received signal by 24, so as to provide a 19KHz three-dimensional demodulated signal; and

an auto-shut off circuit comprising [an] a second IC and a transistor, said transistor being controlled by said second IC to turn a power supply on/off.

24. (amended) The [invention] receiver of claim 21 wherein said [(]external and internal[)] dual adjustable oscillatory frequency regulating circuit has a first intermediate frequency at least above 10MHz

[frequency regulating circuit has the capability to broadly adjust the frequency, and to downconvert]

25. (amended) The [invention] receiver of claim 21 wherein said [(]external and internal[)] dual adjustable oscillatory frequency regulating circuit provides a local oscillatory frequency that can be broadly adjusted without a conventional SAW and which